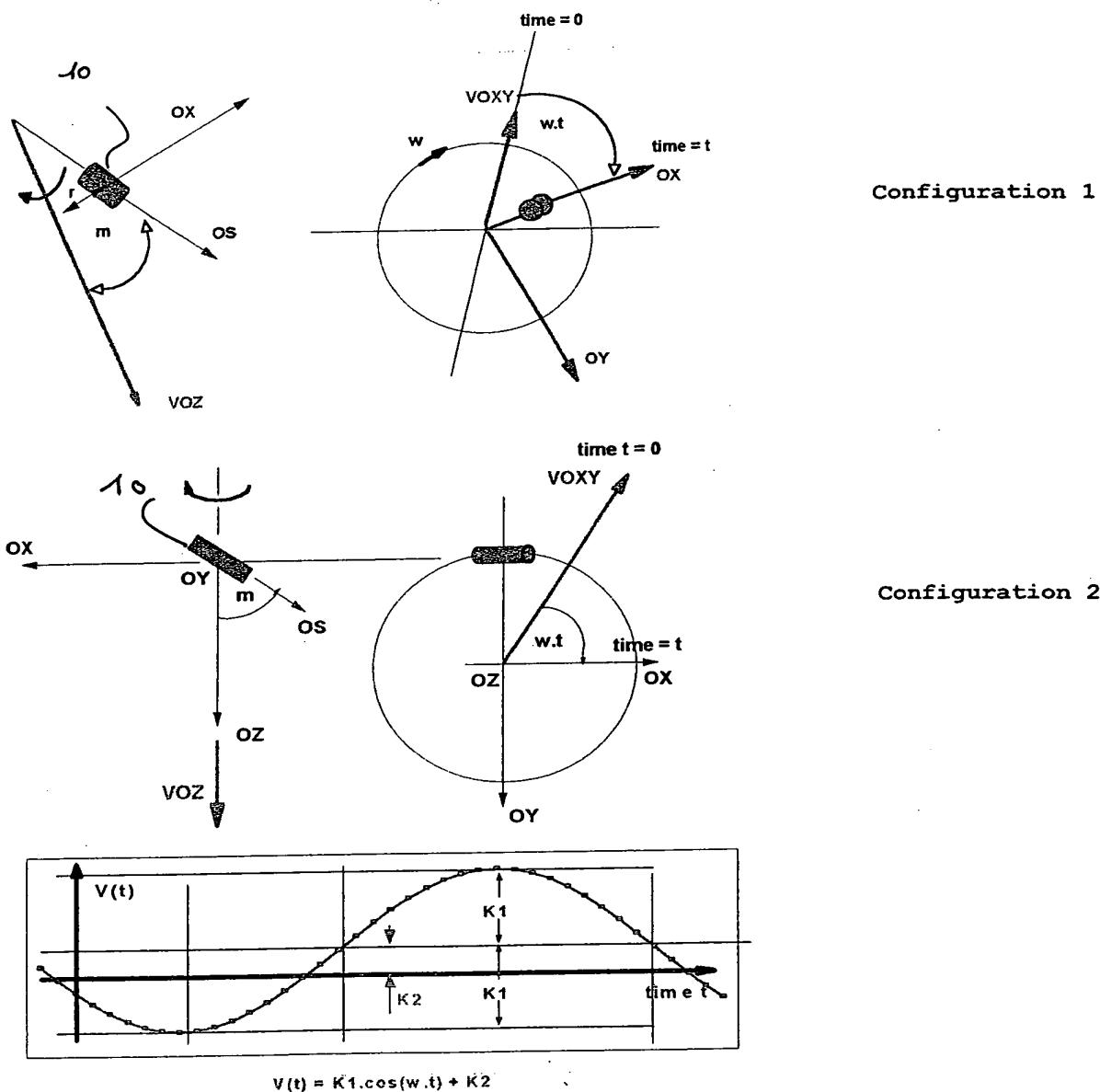


FIG. 1



2/4

## PHASE-LOCKED LOOP

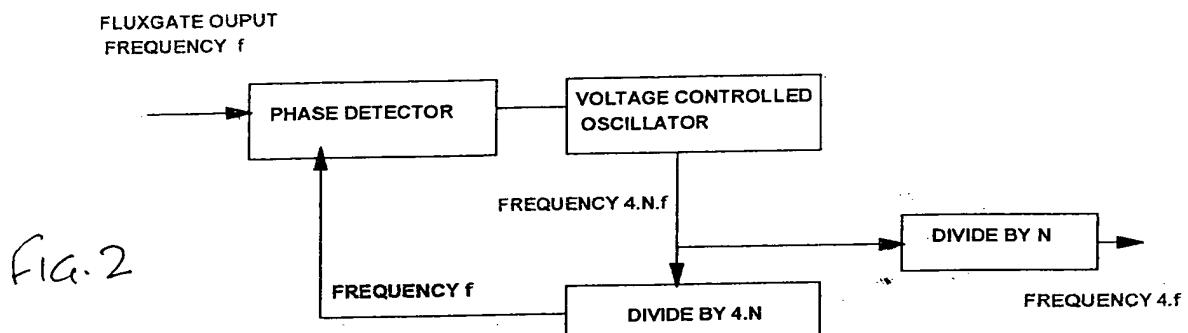
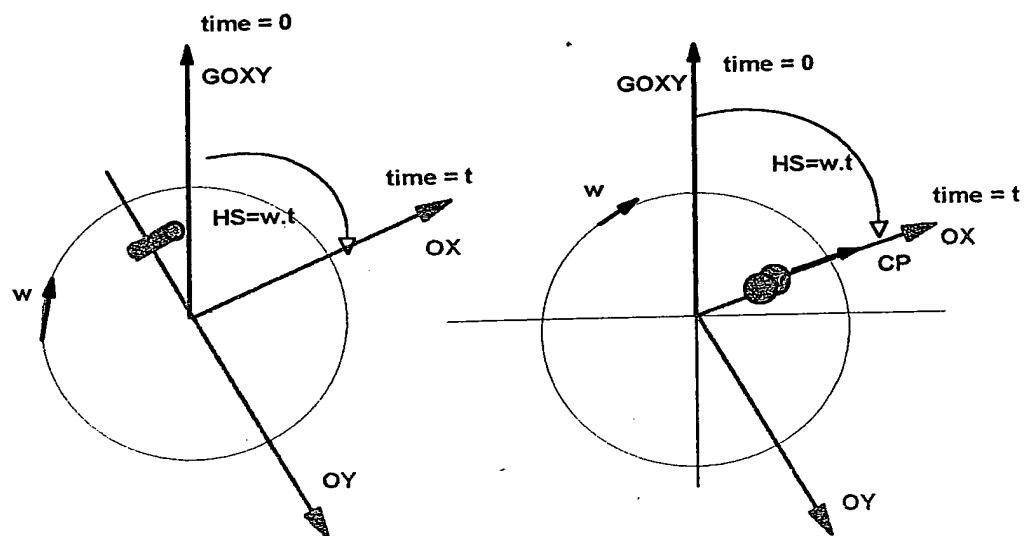


FIG. 3

Configuration 2

Configuration 1



3/4

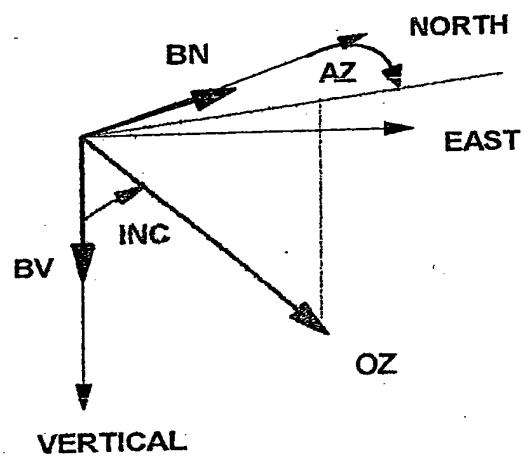


FIG. 4

4/4

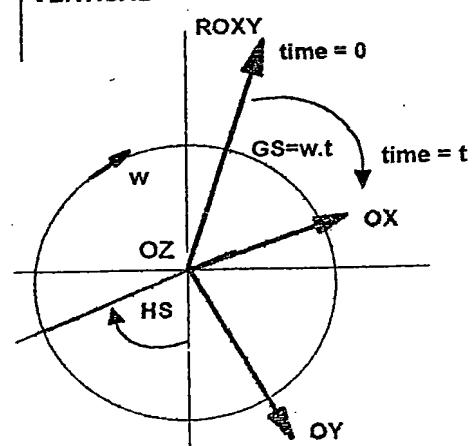
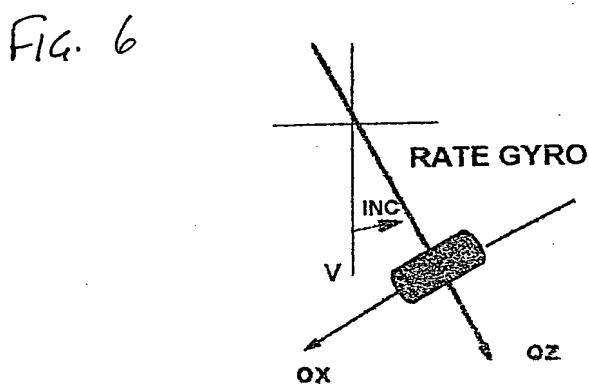
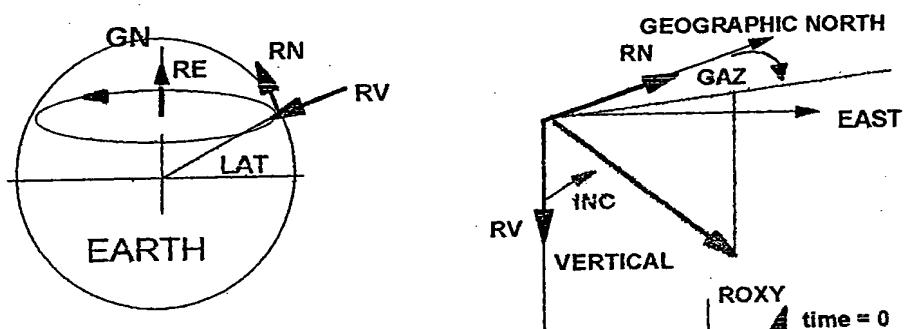
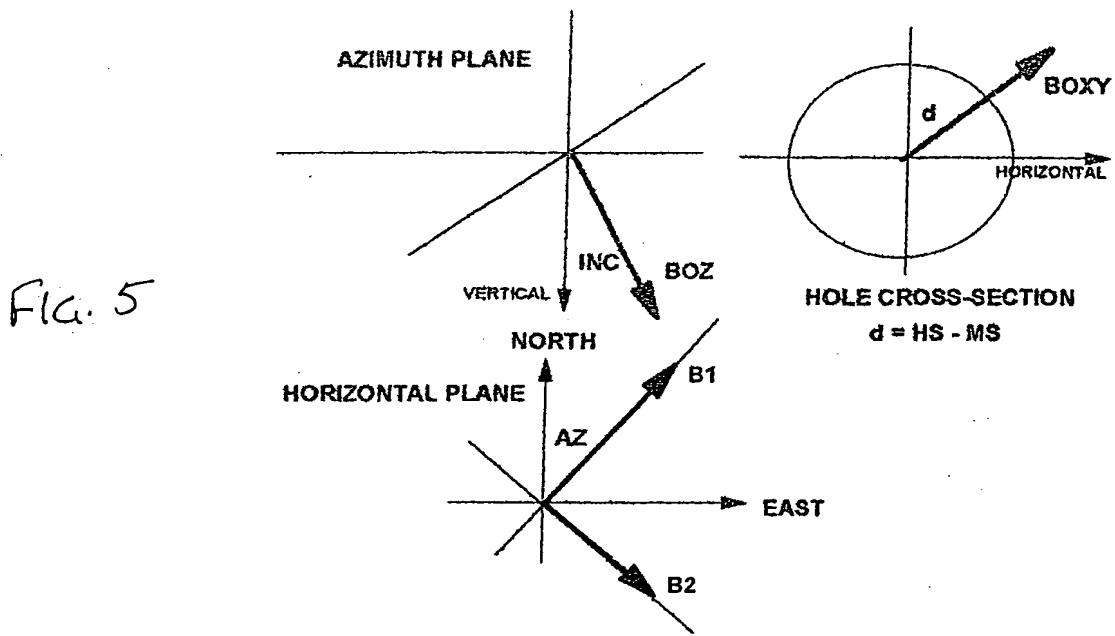


FIG. 6